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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary.)

Complete if Known				
Application Number	10/749,738			
Filing Date	December 30, 2003			
First Named Inventor	Eric Debes			
Art Unit	2193			
Examiner Name	Malzahn, David H			
Attorney Docket Number	42P15771			

Sheet	1	of	15

U.S. PATENT DOCUMENTS

Exam. Initials	Cite No.[1]	Document Number Number-Kind Code (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	AA	US-6,816,961	11/9/2004	Rice et al.	
	AB	US-2003/0131030	7/10/2003	Sebot et al.	
	AC	US-6,745,319	6/1/2004	Balmer et al.	
	AD	US- 2005/0188182	8/25/2005	Hoyle et al.	
	AE	US-2002/0002666	1/3/2002	Dulong et al.	
	AF	US-2003/0123748	7/3/2003	Sebot et al.	
	AG	US-2001/0016902	8/23/2001	Abdallah et al.	
	AH	US-6,211,892	4/3/2001	Huff et al.	
	AI	US-6,115,812	9/5/2000	Abdallah et al.	
	AJ	US-2003/0231711	12/18/2003	Zhang et al.	
	AK	US-2002/0172287	11/21/2002	King	
	AL	US-2002/0159529	10/31/2002	Wang et al.	
	AM	US-			
	AN	US-			
	AO	US-			
	AP	US-			
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FOREIGN PATENT DOCUMENTS

Exam. Initials	Cite No.[1]	Foreign Patent Document Country Code[3]-Number[4]-KindCode[5]	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T[6]
	AU					
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	BA					
	BB					
	BC					
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ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /DHM/

OTHER ART – NO PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	Transl ation²
		AVARO, Olivier, et al., *MPEG-4 Systems Overview and Architecture*, woody.imag.fr/MPEG4/syssite/syspub/docs/tutorial/, 28/05/98, pp. 1-71 plus Yahoo site ref.	
		BIERLING, M., *Displacement Estimation by Hierarchical Blockmatching*, SPIE, Vol. 1001, Visual Communications and Image Processing, May 1998, pp. 942-951.	
		CHAN, Y.L and W.C. Siu, *Adaptive Multiple-Candidate Hierarchical Search for Block Matching Algorithm*, IEE Electronics Letters, Vol. 31, No. 19, Sept. 14, 1995, pp. 1637-1639.	
		CHAN, Yui-Lam and Wan-Chi Siu, *New Adaptive Pixel Decimation for Block Motion Vector Estimation*, IEEE Transactions on Circuits and Systems on Video Technology, Vol. 6, No. 1, Feb. 1996, pp. 113-118.	
		CHEN, Liang-Gee, Wai-Ting Chen, Yeu-Shen Jehng Tzi-Dar Chuieh, *An Efficient Parallel Motion Estimation Algorithm for Digital Image Processing*, IEEE Transactions on Circuits and Systems on Video Technology, Vol. 1, No. 4, Dec. 1991, pp. 378-384.	
		CHENG, K.W., S.C. Chan, *Fast Block Matching Algorithms for Motion Estimation*, ICASSP96, 1996, pp. 2318ff.	
		CORBAL, Jesus, et al., *DLP+TLP Processors for the Next Generation of Media Workloads*, 0-7695-1019-1/01, IEEE, 2001, pp. 219-228.	
		DAY, Neil, Ed., *Introduction to MPEG-7 (v.3.0)*, International Organization for Standardization, ISO/IEC JTC1/SC29/WG11, Coding of Moving Pictures and Audio, #N4032, March 2001, pp. 1-10.	
		DUFAUX, Frederic, et al., *Efficient, Robust, and Fast Global Motion Estimation for Video Coding*, 1057-7149/00, IEEE, 2000, pp. 497-501.	
		ECKART, Stefan, Chad Fogg, *ISO/IEC MPEG-2 Software Video Codec*, SPIE Vol. 2419, Digital Video Compression: Algorithms and Technologies, 1995, San Jose, CA., 15 pages	
		EDIRISINGHE, E.A., et al., *Shape Adaptive Padding for MPEG-4*, 0098 3063/00, IEEE, 2000, pp. 514-520.	
		FENG, J., Lo, K. T. Mehrpour, H. Karbowiak, A.E, *Adaptive Block-Matching Motion Estimation Algorithm for Video Coding*, IEE Electronics Letters, Vol. 31, No. 18, 1995, pp. 1542-1543.	
		FURHT, Botho, Joshua Greenberg, Raymond Westwater, *Motion Estimation Algorithm for Video Compression*, Kluwer Academic Publishers, Boston, 1997, pp. cover-vi, 11, 49-95.	
Examiner Signature			Date Considered

OTHER ART – NO PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	Transl ation⁷
		GHANBARI, M., *The Cross-Search Algorithm for Motion Estimation*, IEEE Transactions on Communications, Vol. 38, No.7, Jul. 1990, pp. 950-953.	
		HE, Zhongli, M.L. Liou, *A High Performance Fast Search Algorithm for Block Matching Motion Estimation*, IEEE Transactions on Circuits and Systems on Video Technology, Vol. 7, No. 5, Oct. 1997, pp. 826-828.	
		HE, Zhong-Li, M.L. Liou, *Design of Fast Motion Estimation Algorithm based on Hardware Consideration*, IEEE Transactions on Circuits and Systems on Video Technology, Vol.7, No.5, Oct. 1997, pp. 819-823.	
		HEISING, G., et al., *MoMuSys: MPEG-4 Version 2 Video Reference Software Package*, AC098/HHI/WP5.1/DS/P/049/B1, 1998, pp. 1-8.	
		INTEL CORPORATION, *Block-Matching in Motion Estimation Alforithms Using Streaming SIMD Extensions 2 (SSE2)*, Vers. 2.0 9/22/00, Order No. 248605-001, pp. 1-13, A-1, A-2.	
		INTERNATIONAL ORGANISATION FOR STANDARDISATION, *Optimization Model, Version 2.0*, ISO/IEC JTC1/SC29/WG11, Coding of Moving Pictures and Audio, #N3675, October 2000, 12 pp.	
		INTERNATIONAL ORGANISATION FOR STANDARDISATION, *New MPEG-4 Profiles Under Consideration*, ISO/IEC JTC1/SC29/WG11, Coding of Moving Pictures and Audio, #N3932, January 2001, pp. 1-35.	
		JAIN, J., A. Jain, *Displacement Measurement and its Application in Interframe Image Coding*, IEEE Transactions on Communications, Vol. 29, No. 12, Dec. 1981, pp. 1799-1808.	
		JU, John C.-H., et al., *A Fast Rate-Optimized Motion Estimation Algorithm for Low-Bit-Rate Video Coding*, 1051-8215/99, IEEE, 1999, pp. 994-1002.	
		JUNG, Hae Mook, Duch Dong Hwang Coong Soo Park, Han Soo Kim, *An Annular Search Algorithm for Efficient Motion Estimation*, International Picture Coding Symposium, PCS96, 1996, pp. 171-174.	
		KAPPAGANTULA, S., K.R. Rao, *Motion Compensated Interframe Image Prediction*, IEEE Transactions on Communications, 33(9), Sept. 1985, pp. 1011-1015.	
Examiner Signature			Date Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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OTHER ART – NO PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No'	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	Translation⁶
		KIM, Joon-Seek, Rae-Hong Park, *A Fast Feature-Based Block Matching Algorithm Using Integral Projections*, IEEE Journal on Selected areas in communications, Vol.10, No.5, June 1992, pp. 968-971.	
		KIM, Michelle, Ed., *MPEG-4 Systems*, International Organization for Standardization, ISO/IEC JTC1/SC29/WG11, Coding of Moving Pictures and Audio, #N3383, June 2000, pp. 1-19.	
		KNEIP, Johannes, et al., *Applying and Implementing the MPEG-4 Multimedia Standard*, 0272-1732/99, IEEE, 1999, pp. 64-74.	
		KNEIP, J. (Johannes), et al., *The MPEG-4 Video Coding Standard—a VLSI Point of View*, IEEE Workshop on Signal Processing Systems (SIPS98), 8-10 Oct. 1998, pp. 43-52, A-1, A-2.	
		KOGA, J., et al., *Motion Compensated Interframe Coding for Video Conferencing*, Proceedings of the National Telecommunications Conference, 1981, pp. G5.3.1- 5.3.3.	
		KOENEN, Rob, Ed., *Overview of the MPEG-4 Standard*, International Organization for Standardization, ISO/IEC JTC1/SC29/WG11, Coding of Moving Pictures and Audio, #N4030, March 2001, pp. 1-69.	
		KUHN, P., *Algorithms, Complexity Analysis and VLSI Architectures for MPEG-4 Motion Estimation*, 1999 Kluwer Academic Publishers, Boston, pp. cover-vi, 15, 17-59, 107-109, 119-121, 147-167, and 189-204.	
		KUHN, P., Stechele W., *Complexity Analysis of the Emerging MPEG-4 Standard as a Basis for VLSI Implementation*, vol. SPIE 3309 Visual Communications and Image Processing, San Jose, Jan.1998, pp. 498-509.	
		LEE, Liang-Wei, Jhing-Fa Wang, Jau- Yien Lee, Jung-Dar Shie, *Dynamic Search-Window Adjustment and Interlaced Search Block-Matching Algorithm*, IEEE Transactions on circuits and systems for video technology, Vol. 3, No. 1, Feb. 1993, pp. 85-87.	

Examiner Signature		Date Considered	

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /DHM/

OTHER ART – NO PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	Translation
		LEE, W., Y. Kim, R.J. Gove, C.J. Read, *Media Station 5000: Integrating Video and Audio*, IEEE Multimedia, Vol. 1, No. 4, 1994, pp. 50-61.	
		LEE, Xiaobing, Ya-Qin Zhang, *A Fast Hierarchical Motion-Compensation Scheme for Video Coding Using Block-Feature Matching*, IEEE Transactions on Circuits and Systems for Video Technology, Vol. 6, No. 6, Dec. 1996, pp. 627-635.	
		LENGWEHASATIT, Krisda, et al., *A Novel Computationally Scalable Algorithm for Motion Estimation*, SPIE 3309 VCIP Visual Communications and Image processing, San Jose, CA, Jan. 1998, pp. 66-79.	
		LI, R., B. Zeng, M.L. Liu, *A New Three-Step Search Algorithm for Block Motion Estimation*, IEEE Transactions on Circuits and Systems on Video Technology, Vol. 4, No. 4, Aug. 1994, pp. 438-442.	
		LI, W., E. Salari, *Successive Elimination Algorithm for Motion Estimation*, IEEE Trans. Image Processing, Vol. 4, Jan. 1995, pp. 105-107.	
		LIANG, Jie, et al., *Region-Based Video Coding with Embedded Zero-Trees*, 1068-0314/97, IEEE, 1997, p. 449.	
		LIU, B., A. Zaccarin, *New Fast Algorithms for the Estimation of Block Motion Vectors*, IEEE Transactions on Circuits and Systems on Video Technology, Vol.3, No.2, April 1993, pp. 148-157.	
		LIU, Lurng-Kuo, Ephraim Feig, *A Block-Based Gradient Descent Search Algorithm for Block-Based Motion Estimation in Video Coding*, IEEE Transactions on Circuits and Systems on Video Technology, Vol. 6, No. 4, Aug. 1996, pp. 419-422.	
		MO, Hyeon-Cheol, et al., *A High-Speed Pattern Decoder in MPEG-4 Padding Block Hardware Accelerator*, 0-7803-6685-9/01, IEEE, 2001, pp. II-197 – II-200.	
		MOSCHETTI, F., et al., *About Macroblock Subsampling for Motion Estimation on IA-64*, Proc. of 2001 IEEE Int'l. Conf. on Multimedia and Expo ((ICME 2001), Tokyo, Japan, August 2001, 4 pp.	
		MOSCHETTI, F., et al., *A Fast Block Matching for SIMD Processors Using Subsampling*, IEEE 0-7803-5482-6/99, pp. IV-321 – IV-324., 2000.	
		NAM, Kwon Moon, Joon-Seek Kim, Rae-Hong Park, Young Serk Shim, *A Fast Hierarchical Motion Vector Estimation Algorithm Using Mean Pyramid*, IEEE Transactions on Circuits and Systems on Video Technology, Vol. 5, No. 4, Aug. 1995, pp. 344-351.	

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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.
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		First Named Inventor: Eric Debes	Examiner: Malzahn, David H.
Page 6		Filing Date: December 30, 2003	Art Unit: 2193
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		NETRAVALI, A., B. Haskell, *Digital Pictures Representation and Compression*, New York, Plenum, 1988, pp. cover-xv, 334-340, 537-542, and 354-355.	
		PIRSCH, Peter, Nicolas Demassieux, Winfried Gehrke, *VLSI Architectures for Video Compression - A Survey*, Proceedings of the IEEE, Vol. 83, No. 2, Feb. 1995, pp. 220-246.	
		PO, Lai-Man, Wing-Chung Ma. *A Novel Four-Step Search Algorithm for Fast Blockmatching*, IEEE Transactions on Circuits and Systems on Video Technology, Vol. 6, No. 3, Jun. 1996, pp. 313-317.	
		PURI, A., H.M. Hang, D.L. Schilling, *An Efficient Blockmatching Algorithm for Motion Compensated Coding*, Proc. IEEE ICASSP, 1987, pp. 2.4.1-25.4.4.	
		RAGSDALE, Gary L., et al, *Relationships of Popular Transmission Characteristics to Perceived Quality for Digital Video Over ATM*, National Communications System, Technical Information Bulletin 99-2, January 1999, 64 pp.	
		RAMKISHOR, K., et al., *Real Time Implementation of MPEG-4 Video Decoder on ARM7TDMI*, Proc. of 2001 Int'l. Symposium on Intelligent Multimedia, Video and Speech Processing, May 2-4, 2001, pp. 522-526.	
		SHI, Y.Q., X. Xia, *A Thresholding Multiresolution Block Matching Algorithm*, IEEE Transactions on Circuits and Systems on Video Technology, Vol. 7, No. 2, April 1997, pp. 437-440.	
		SIKORA, Thomas, *MPEG Digital Video Coding Standards*, Preprint from Digital Consumer Electronics Handbook, 1ˢᵗ Ed., McGraw-Hill Book Co., Ch. 9, pp. 1-43.	
		SIKORA, Thomas, *MPEG-1 and MPEG-2 Digital Video Coding Standards*, Preprint from Digital Consumer Electronics Handbook, 1ˢᵗ Ed., McGraw-Hill Book Co., pp. 1-43.	
		SIKORA, Thomas, *The Structure of the MPEG-4 Video Coding Algorithm*, Preprint from Digital Consumer Electronics Handbook, 1ˢᵗ Ed., McGraw-Hill Book Co., pp. 1-16.	
Examiner Signature			Date Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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Examiner Initials*	Cite No¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	Trans lation*
		SONG, Byung Cheol, Jong Beom Ra, *A Hierarchical Block Matching Algorithm Using Partial Distortion Criteria*, SPIE 3309 VCIP Visual Communications and Image Processing, 1998, San Jose, CA, pp. 88-95.	
		SRINIVASAN, Ram and K.R. Rao, *Predictive Coding Based on Efficient Motion Estimation*, IEEE Transactions on Circuits and Systems on Video Technology, Vol. Com-33, No. 8, Aug. 1985, pp. 888-896.	
		STOLBERG, H.-J., et al., *The M-Pire MPEG-4 Codec DSP and Its Macroblock Engine*, 0-7803-548206/99, IEEE, 2000, pp. II-192-II-195.	
		THAM, Jo Yew, et al., *Transactions Letters: A Novel Unrestricted Center-Biased Diamond Search Algorithm for Block Motion Estimation*, IEEE, 1051-8215/98, 1998, pp. 369-377.	
		van der SCHAAR, M., et al., *Near-Lossless Complexity-Scalable Embedded Compression Algorithm for Cost Reduction in DTV Receivers*, 0098 3063/00, IEEE, 2000, pp. 923-933.	
		WANG, Chung-Neng, et al., *Improved MPEG-4 Visual Texture Coding Using Double Transform Coding*, 0-7803-6685-9/01, IEEE, 2001, pp. V-227 – V-230.	
		WESTERINK, P. H., et al., *Two-Pass MPEG02 Variable-Bit-Rate Encoding*, IBM J. Res. Develop, Vol. 43, No. 4, July 1999, pp. 471-488.	
		WITTENBURG, J.P., et al., *HiPAR-DSP: A Parallel VLIW·RISC Processor for Real Time Image Processing Applications*, (0-7803-4229-1/97) IEEE, 1997, pp. 155-162.	
		XU, Jie-Bin, Lai-man Po, and Chok-Kwan Cheung, *A New Prediction Model Search Algorithm for Fast Block Motion Estimation*, IEEE Int. Conf. Image Processing, ICIP97, Santa Barbara, 1997., pp 610 -613.	
		YU, Fengqi and Alan N. Willson, Jr., *A Flexible Hardware-Oriented Fast Algorithm for Motion Estimation*, ICASSP97, 1997, pp. 2681ff.	
		ZHU, Shan, Kai-Kuang Ma, *A New Diamond Search Algorithm for Fast Block Matching*, IEEE Transactions on Circuits and Systems on Video Technology, Vol. 9, No. 2, Feb. 2000, pp. 287-290.	

Examiner Signature		Date Considered	

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /DHM/

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U.S.PATENTS						Remove
Examiner Initial*	Cite No	Patent Number	Kind Code¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear
	1	5019968		1991-05-28	Wang, et al.	
	2	5321810		1994-06-14	Case, et al.	
	3	5497497		2008-01-23	Miller, et al.	
	4	6381690		2002-04-30	Lee	
	5	6223277		2001-04-24	Karguth	

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	1	20030084082		2003-05-01	Debes, Eric; et al.	

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	2	20020112147		2002-08-15	Chennupaty, Srinivas; et al.	

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	1	PHILIPS ELECTRONICS, "TriMedia TM1000 Preliminary Data Book," 1997, 70 pgs	☐
	2	"MIPS Digital Media Extension," Set Architecture Specification, Web Site - mips.com/MDMXspec.ps, October 21, 1997. 8 pgs	☐
	3	HEWLET PACKARD, "64-bit and Multimedia Extensions in the PA-RISC 2.0 Architecture," Microprocessors Precision Architecture, 1997, 18 pgs	☐
	4	KAWAKAMI, Y., ET AL., "A Single-Chip Digital Signal Processor for Voiceband Applications,"IEEE, 1980 International Solid-State Circuits Conference, pp. 40-41, 1980.	☐
	5	SUN MICROSYSTEMS, INC., "UltraSPARC Multimedia Capabilities On-Chip Support for Real-Time Video and Advanced Graphics," SPARC Technology Business, September 1994, 8 pgs	☐

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /DHM/

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	6	CASE, B., "Philips Hopes to Displace DSPs with VLIW, TriMedia Processors Aimed at Future Multimedia Embedded Apps," Microprocessor Report, December 1994, pp. 12-18	☐
	7	Gwennap, L., "New PA-RISC Processor Decodes MPEG Video, H"'s PA-7100LC Uses New Instructions to Eliminate Decoder Chip," Microprocessor Report, January, 1994, pp. 16-17	☐
	8	TEXAS INSTRUMENTS, "TMS320C2X User's Guide," 1993, pp 3:2-3:11; 3:28-3:34; 4:1-4:22; 4:41; 4:103; 4:119-J:120; 4:122; 4:150-4:151	☐
	9	INTEL CORPORATION, "i860 TM Microprocessor Family Programmer's Reference Manual," 1992, Chapters 1, 3, 8 and 11	☐
	10	LEE, R. B., "Accelerating Multimedia with Enhanced Microprocessors," IEEE Micro, April 1995, pp. 22-32	☐
	11	INTEL CORPORATION, "Pentium Processor's User's Manual, vol. 3: Architecture and Programming Manual," 1993, Chapters 1, 3, 4, 6, 8, and 18	☐
	12	MARGULIS, N., "i860 Microprocessor Architecture," McGraw Hill, Inc., 1990, Chapters 6, 7, 8, 10, and 11	☐
	13	INTEL CORPORATION, "Intel i750, i860 TM, i960 Processors and Related Products, 1993, pp. 1-3	☐
	14	MOTOROLA, INC., "Motorola MC88110 Second Generation RISC Microprocessor User's Manual," 1991, 600 pages.	☐
	15	MOTOROLA, INC., "Errata to MC88110 Second Generation RISC Microprocessor User's Manual," 1992, pp. 1-11	☐
	16	MOTOROLA, INC., "MC88110 Programmer's Reference Guide, 1992, pp. 1-4	☐

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ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /DHM/

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	Attorney Docket Number	42P15771

	17	SHIPNES, J., "Graphics Processing with the 88110 RISC Microprocessor," Motorola, Inc. IEEE, No. 0-8186-26455-0/92, 1992, pp. 169-174	☐
	18	ADVANCED MICRO DEVICES, INC., "AMD-3D Technology Manual," February 1998, pp. 1-58	☐
	19	HANSEN, C., "Architecture of a Broadband Mediaprocessor," Proceeedings of Compcon, IEEE, 1996, pp. 334-340	☐
	20	LEVINTHAL, ET AL., "Chap--A SIMD Graphics Processor," Computer Graphics Project, ACM, vol. 18, No. 3, July 1984, pp. 77-81	☐
	21	LEVINTHAL, ET AL., "Parallel Computers for Graphics Applications," Proceedings: Second International Conference on Architectural Support for Programming Languages and Operating Systems, (ASPLOS II), IEEE, 1987, pp. 193-198	☐
	22	WANG, ET AL., "A Processor Architecture for 3D Graphics Calculations," Computer Motion, Inc., Goleta, CA, 23 pgs	☐

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number	10 749,738
	Filing Date	December 30, 2003
	First Named Inventor	Eric Debes
	Art Unit	2193
	Examiner Name	Malzahn, David H.
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Examiner Initial*	Cite No	Patent Number	Kind Code¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear
	1	3711692		1973-01-16	BATCHER	
	2	3723715		1973-03-27	CHEN et al.	
	3	4139899		1979-02-13	TULPULE et al.	
	4	4161784		1979-07-17	CUSHING et al	
	5	4393468		1983-07-12	NEW	
	6	4418383		1983-11-29	DOYLE et al.	
	7	4490786		1984-12-25	NAKATANI	
	8	4498177		1985-02-05	LARSON	

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	9	4707800		1987-11-17	MONTRONE et al.	
	10	4771379		1988-09-13	ANDO et al.	
	11	4903228		1990-02-20	GREGOIRE et al.	
	12	4989168		1991-01-29	KURODA et al.	
	13	5081698		1992-01-14	KOHN	
	14	5095457		1992-03-10	JEONG	
	15	5168571		1992-12-01	HOOVER et al.	
	16	5187679		1993-02-16	VASSILIADIS et al.	
	17	5268995		1993-12-07	DIEFENDORFF et al.	
	18	5390135		1995-02-14	LEE et al.	
	19	5408670		1995-04-18	DAVIES	

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20	5423010		1995-06-06	MIZUKAMI	
21	5426783		1995-06-20	NORRIE et al.	
22	5465374		1995-11-07	DINKJIAN et al.	
23	5487159		1996-01-23	BYERS et al.	
24	5524256		1996-06-04	TURKOWSKI	
25	5579253		1996-11-26	LEE et al.	
26	5594437		1997-01-14	O'MALLEY	
27	5625374		1997-04-29	TURKOWSKI	
28	5680161		1997-10-21	LEHMAN et al.	
29	5781457		1998-07-14	COHEN et al.	
30	5819117		1998-10-06	HANSEN	

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	31	5822619		1998-10-13	SIDWELL	
	32	5838984		1998-11-17	NGUYEN et al.	
	33	5909572		1999-06-01	THAYER et al.	
	34	5933650		1999-08-03	van HOOK et al.	
	35	6041404		2000-03-21	ROUSSEL et al.	
	36	6115812		2000-09-05	ABDALLAH et al.	
	37	6192467		2001-02-20	ABDALLAH et al.	
	38	6288723		2001-09-11	HUFF et al.	
	39	6484255		2002-11-19	DULONG	
	40	6546480		2003-04-08	MANDAVILLI et al.	

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